



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Aniruddha P. Joshi et al.

§

Art Unit: 2622

Serial No.: 09/583,432

§

Examiner: Sherrie Y. Hsia

Filed: May 31, 2000

§

Docket: ITL.0361US
P8580

For: Power Management for
Processor-Based Appliances

§

Assignee: Intel Corporation

§

Mail Stop Appeal Brief-Patents

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

RESPONSE TO DECISION ON APPEAL

Sir:

In response to the Decision on Appeal dated December 28, 2007, please amend the above-referenced patent application as follows:

Date of Deposit: February 20, 2008

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Cynthia L. Hayden

IN THE CLAIMS

1 (Previously Presented). A method comprising:

in response to operation of a power button, transitioning a processor-based system from a lower power consumption state to a higher power consumption state; and

in response to re-operation of said power button, transitioning said processor-based system from said higher power consumption state to said lower power consumption state;

transitioning said processor-based system from the lower power consumption state to a still lower power consumption state in response to a lack of activity on said processor-based system; and

transitioning from said still lower power consumption state to said lower power consumption state whenever a television receiver is operating.

Claim 2 (Canceled).

3 (Previously Presented). The method of claim 1 including transitioning said system from said still lower power consumption state back to said lower power consumption state if activity is detected around said processor-based system.

4 (Original). The method of claim 3 including detecting motion around said processor-based system.

5 (Previously Presented). The method of claim 1 including transitioning said system from said still lower power consumption state back to said lower power consumption state if light is detected around said processor-based system.

Claim 6 (Canceled).

7 (Previously Presented). The method of claim 1 including preventing said system from going to a power off state in response to operation of the power button.

8 (Original). The method of claim 1 including receiving a power command from a power button on a remote control unit.

9 (Original). The method of claim 1 wherein said system includes an operating system, said method including providing a power management module in connection with the operating system for said processor-based system to handle power management events.

10 (Original). The method of claim 9 wherein said power management module responds to power management events by passing control to a boot loader.

11 (Previously Presented). An article comprising a medium that stores instructions that, if executed, enable a processor-based system to:

in response to operation of a power button, transition said processor-based system from a lower power consumption state to a higher power consumption state; and

in response to re-operation of said power button, transition said processor-based system from said higher power consumption state to said lower power consumption state;

transition from the lower power consumption state to a still lower power consumption state in response to a lack of activity on said system; and

transition from still lower power consumption state to said lower power consumption state in response to operation of a television receiver.

Claim 12 (Canceled).

13 (Previously Presented). The article of claim 11 further storing instructions that cause the processor-based system to transition from said still lower power consumption state back to said lower power consumption state if activity is detected around said processor-based system.

14 (Previously Presented). The article of claim 13 further storing instructions that cause the processor-based system to detect motion around said processor-based system.

15 (Previously Presented). The article of claim 11 further storing instructions that cause the processor-based system to transition from said still lower power consumption state back to said lower power consumption state if light is detected around said processor-based system.

Claim 16 (Canceled).

17 (Previously Presented). The article of claim 11 further storing instructions that prevent said system from going to a power off state in response to operation of the power button.

18 (Original). The article of claim 11 further storing instructions that cause said processor-based system to receive a power on command from the power button on a remote control unit.

19 (Previously Presented). The article of claim 11 further storing instructions that cause the processor-based system to transition between said lower and higher power consumption states using a software module at an operating system kernel level.

20 (Original). The article of claim 19 further storing instructions that cause said processor-based system to respond to power management events by passing control to a boot loader.

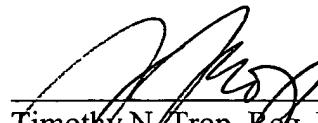
Claims 21-40 (Canceled).

REMARKS

Rejected claims 31-35 have been canceled.

Therefore, the application should now be in condition for allowance.

Respectfully submitted,



Date: February 20, 2008

Timothy N. Trop, Reg. No. 28,994
TROP, PRUNER & HU, P.C.
8554 Katy Freeway, Ste. 100
Houston, TX 77024
713/468-8880 [Phone]
713/468-8883 [Fax]

Attorneys for Intel Corporation